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# Simulink Model Audio Compression Using Wavelet Transform

Peer Reviewed Journal IJERA com. Free Range Factory. Volume 3 Issue 1 International Journal of Engineering. Browse by Thesis Type ethesis. Peer Reviewed Journal IJERA com. EMBC 17 Program Thursday July 13 2017. Contents. ICRTES

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**May 10th, 2018 - International Journal of Engineering Research and Applications IJERA is an open access online peer reviewed international journal that publishes research'**

**'Free Range Factory**

**May 11th, 2018 - arithmetic core lphaAdditional info FPGA provenWishBone Compliant NoLicense LGPLDescriptionRTL Verilog code to perform Two Dimensional Fast Hartley Transform 2D FHT for 8x8 points Presented algorithm is FHT with decimation in frequency domain Main FeaturesHigh Clock SpeedLow Latency 97 clock cycles Low Slice CountSingle Clock Cycle per'**

**'Volume 3 Issue 1 International Journal of Engineering**

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May 10th, 2018 - The conference is postponed to 20 th of  
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any further enquiries Inconvenience caused deeply  
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